

GP 2811



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Akihiro MURATA

Group Art Unit: 2811

Application No.: 09/245,288

Examiner: M. Dietraich

Filed: February 5, 1999

Docket No.: 101937

For: SEMICONDUCTOR APPARATUS SUBSTRATE, SEMICONDUCTOR APPARATUS, AND METHOD OF MANUFACTURING THEREOF

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RESPONSE TO RESTRICTION REQUIREMENT AND AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In reply to the Office Action mailed May 4, 2000, please amend the above-identified application as follows:

IN THE CLAIMS:

Please cancel claims 13-18.

Please add the following claims 19-25:

- Sub B3
G1
- 19. A method of manufacturing semiconductor apparatus, comprising:
preparing a substrate having a mounting surface for mounting the
semiconductor device;
forming a plurality of leads on the mounting surface; and
forming a plurality of conduction sections each defining at least part of an
external terminal, and electrically connecting the conduction sections to the leads.--
- 20. The method of claim 19, further comprising:
mounting a semiconductor device on the substrate;

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